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No. 1 | 2018 - FPGA Verification Day

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Dear Sir or Madam,

We would like to invite you to attend

FPGA Verification Day 2018.

It is a one day only event on **14th March 2018 in Berlin, Germany**, organized in co-operation with Mentor - a Siemens Business.

Leading international industry experts will be focusing on the subject of FPGA Verification, presenting topics such as Safety Critical Design and Verification Flow, Portable Stimulus Specification - the Next Big Wave in Functional Verification, Test Bench Acceleration, Verification Management, as well as a number of other topics.. For the complete agenda and further details please click [here](#)

Presentations will be held by Brian Mathewson (USA), Espen Tallaksen (Norway), Dr. Vladimir Poučki (Germany) and Stefan Bauer (Germany). They will also be available for your individual questions during the open Q & A Session.

Admission is free and there is a lucky draw at the end with the chance to win a 2nd generation Echo Dot.


▶ **FPGA Verification Day 2018**
14th March 2018
Berlin, Germany

Info &
Registration

***We have a limited number of rooms available at a special rate - please contact us for information**

Our dates for workshops and trainings 2018 can be found on our website as usual. Of course we can also offer in-house training for all subjects, please contact us with your requirements.

Contact us:

 +40-332-44 01 49

 info@trias-micro.com

Best regards

Mircea Slanina

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