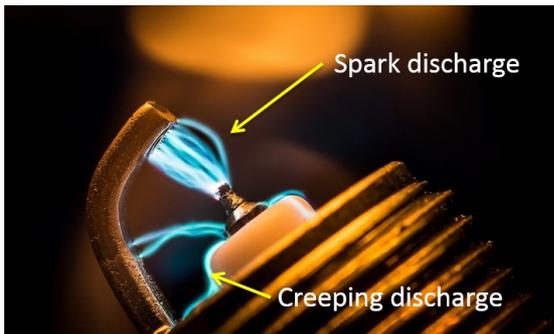




SAFETY, CREEPAGE AND 3D CLEARANCE CHECKS



Dear Sir or Madam,

Safety is paramount when designing electronic systems that use high voltages, which is why these designs must comply with safety standards to ensure humans are at no risk when operating those systems.

Creepage is a phenomenon which allows high voltage to travel the longer path, along the surface of an insulator (e.g. FR4), and reach metal areas which could be in contact with humans, thus endangering their lives. The actual verifications to prevent creepage are by no means trivial and several variables must be accounted for: minimum creepage distance, minimum clearance distance, contamination, peak voltage, RMS voltage, even altitude! And this is not only on the same layer, voltage can travel round the board or through the board, from top to bottom.

Creepage is a phenomenon which allows high voltage to travel the longer path, along the

Any visual inspection will struggle to detect **ALL** violations on a crowded board, while an automated verification can detect even the hardest-to-find errors and save weeks off your PCB design time.

In the on-demand webinar you can see examples of safety violations which escaped manual / visual inspection.

if you're short on time, then watch the technology overview video to learn how HyperLynx DRC can help you check compliance with IEC60950 / IEC62368 standards.

Webinar: Safety, Creepage, and Clearance Checks for PCB Design

**VIEW
WEBINAR**

Video: Multi-layer safety clearance and creepage rules

VIEW VIDEO

Please also visit our website to see details of the public training sessions we can offer. The courses are available as in-house training as well - please contact us to discuss your requirements.

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Best regards
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