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September | 2020 - FPGA Verification Day 2020 - ONLINE

**FPGA VERIFICATION DAY** "Digital" - 24 September 2020



Spaces for FPGA Verification Day 2020, which this year is **LIVE online**, are filling up fast. If you have not registered yet, you should do it now, because places are limited.

Don't miss the Wilson Research Group's latest findings on FPGA Verification Trends, Challenges and Solutions, presented by **Harry Foster** and **Stefan Bauer**. **Espen Tallaksen** will then bring you closer to the subject of UVVM and with it the reduction of verification time.

**Tobias Baumann** will deal with the approval of an FPGA-based medical product, since the IEC 62304 standard is a major topic in medical technology. In addition, **Hans-Jürgen Schwender** will bring you details about the integration of a requirements management segment flow, and how the tracking is achieved from the results of the verification up to the reports of the coverage metrics.

As always, there is something to be won in addition to the interesting presentations. Do you want to know more? Register now!

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[Register](#)

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Best regards and stay safe  
Team TRIAS

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