



April | 2021

**WEBINAR SERIES: FPGA Verification Made Modern 2021**



What do you think of the webinar series **FPGA Verification Made Modern** so far?

We have worked closely with Siemens EDA (formerly Mentor Graphics) to hopefully offer you a good selection of topics presented by technical experts.

Next week it will be half time in the series already, so remember to register for the remaining

sessions.

One topic stands out in particular, and that is why **CDC (Clock Domain Crossing)** is so important for FPGA design verification.

Make a note of the date - 11th May at 2pm!

▶ [Webinar Serie - FPGA Verification Made Modern 2021](#)


[Information | Registration](#)

In addition to these webinars, we offer a wide range of [online training courses](#) on a variety of topics. Check it out yourself or contact us.

Best regards and stay safe!

Pia Pacovsky

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