

MODELSIM/QUESTA CORE: ADVANCED TOPICS

DESCRIPTION

ModelSim/Quarta Core: Advanced Topics teaches you to capitalize on the extensive capabilities of ModelSim/Quarta Core to effectively and efficiently analyze and debug digital HDL designs. Using various ModelSim/Quarta Core features and techniques, you will learn how to produce higher performance test benches, more reliable device-under-test models, and greater confidence in simulation thoroughness and completeness.

AGENDA

- ▶ Use advanced debugging concepts and methods
- ▶ Address advanced design topics and issues
- ▶ Take advantage of advanced cross-window capabilities supporting debugging
- ▶ Manipulate designs and the ModelSim environment using Tcl/Tk
- ▶ Customize design monitors and comparators using Tcl/Tk
- ▶ Determine design "Code Coverage" for verification scenarios
- ▶ Use ModelSim in debugging and performance modes
- ▶ Use the profiler to find bottlenecks in your code
- ▶ Use "Virtual Objects" to explore designs under test
- ▶ Perform advanced design probing with "Signal Spy"
- ▶ Create and compare multiple datasets
- ▶ Use advanced waveform comparison features
- ▶ Visualize and debug Finite State Machines with the FSMViewer
- ▶ Use ModelSim for simulating VHDL, Verilog, and SystemVerilog designs
- ▶ Analyze and improve design and end product performance from high-level abstract design description through gate-level implementations
- ▶ Tracing the cause of any signal event or all possible drivers of a signal
- ▶ Debug multiple types of specific design errors

PREREQUISITES

- ▶ VHDL or Verilog knowledge
- ▶ Beginner skills in ModelSim/Quarta Core or take the ModelSim/Quarta Core: HDL Simulation training class in advance

DURATION

2 Days

LANGUAGE

English or German

COSTS

Upon request*

www.trias-mikro.com

**including training materials*



TRAINER

Alexandru Vlad Velea

has an Electronics, Telecommunications and Information Technology University degree followed by MBA postgraduate degree.

From 2005 on he has been covering mostly the following Siemens products:

- HDL design, simulation and synthesis
- Wiring and harness design

He has a bright knowledge as consultant/ advisor/ technical support/ tools trainer. He is Wiring Harness consultant/ advisor for the Mentor Graphics / Siemens tools since 2011 and Digital IC flow (design/ simulation/ synthesis) consultant/ advisor for the Mentor Graphics/ Siemens tools starting 2005.

TRIAS is an Expert Partner of Siemens Digital Industries Software. Siemens Digital Industries Software awards the status "Expert" to sales partners who have in-depth expert knowledge in a product area or industry and have proven this repeatedly in reference projects.

Alexandru Vlad Velea is certified by Siemens for the products Capital | Capital Essentials (formerly VeSys®) for the automotive and aerospace (Aero) markets and continuously undergoes a mandatory certification program to verify and expand his competencies.

ADDITIONAL COURSE

- ▶ **ModelSim/ Questa Core: HDL Simulation**

Subject to change



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